



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/067,680

) Date: March 31, 2004

Applicant: Tsung-Pei Chiang, et al.

) Examiner: Kevin M. Nguyen  
Group Art Unit 2674

Filed: 02/04/2002

) Our Ref: 619511-2 B-4493

For: "Driving Method for A Power-Saving  
Thin Film Transistor Array"

PATENT

2674  
4/13-04

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2600

Sir:

This paper is filed in response to the Office Action dated December 31, 2003. Please amend the above-identified application as described below. **All amendments and remarks herein are made without prejudice.**

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 6 of this paper and include an attached replacement sheet.

**Remarks** begin on page 7 of this paper.

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Fee Charge  
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The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to deposit account No. 12-0415. In particular, if this response is not timely filed, then the Commissioner is authorized to treat this response as including a petition to extend the time period pursuant to 37 CFR 1.136(a) requesting an extension of time of the number of months necessary to make this response timely filed and the petition fee due in connection therewith may be charged to deposit account no. 12-0415.

I hereby certify that this correspondence is being deposited with the United States Post Office with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on  
March 31, 2004  
(Date of Deposit)

Troy Guangyu Cai  
(Name of Person Signing)

  
(Signature)

3/31/04  
(Date)

Respectfully submitted,



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